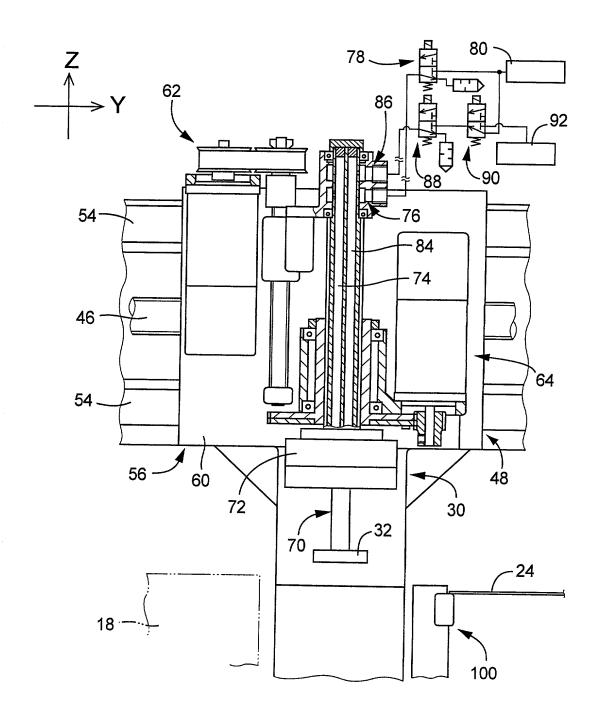
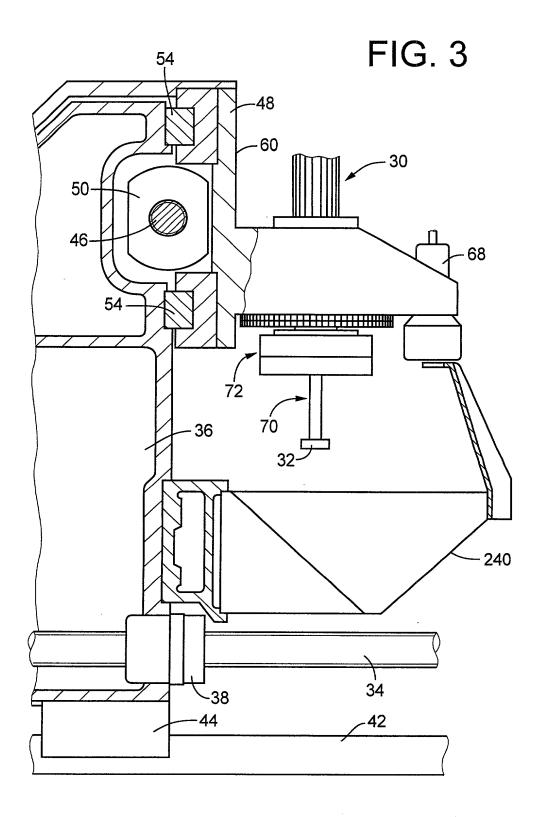
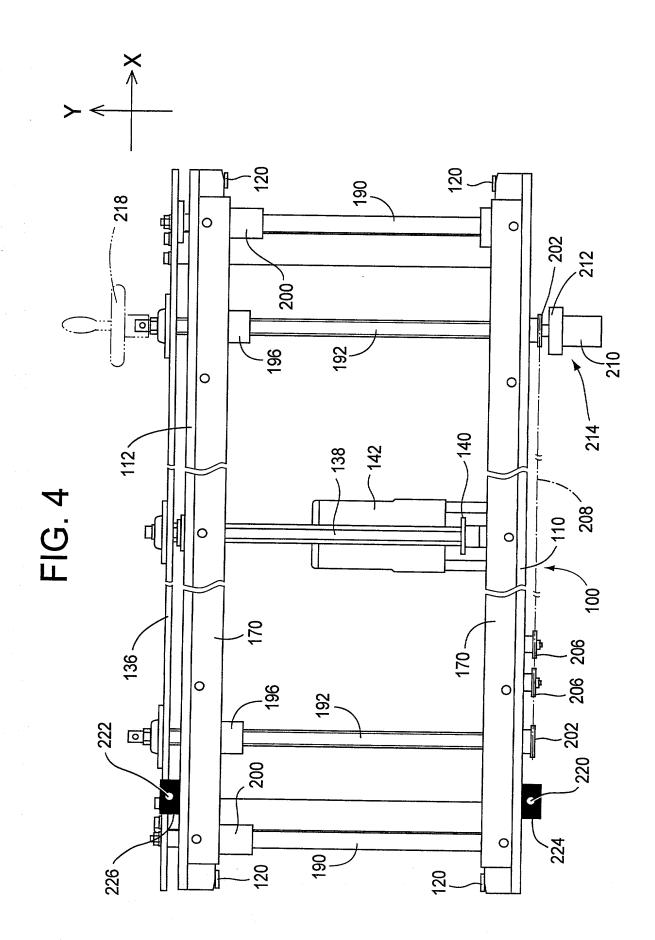


and the state of t

FIG. 2

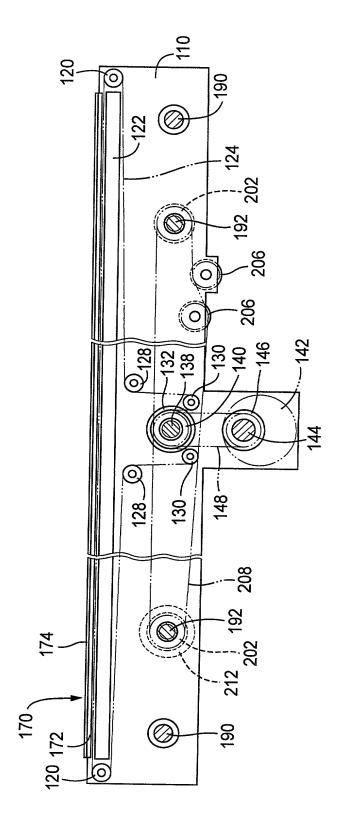






136 172 1,4 100 158 160 FIG. 5 -162 .-24 138 7--148 ~124 ~120 130 172

FIG. 6



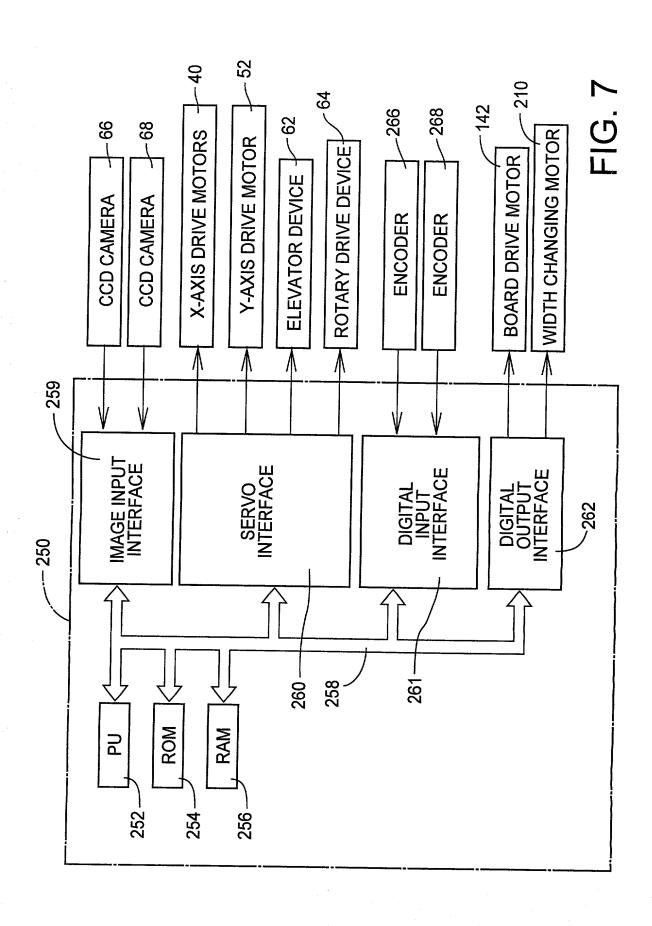
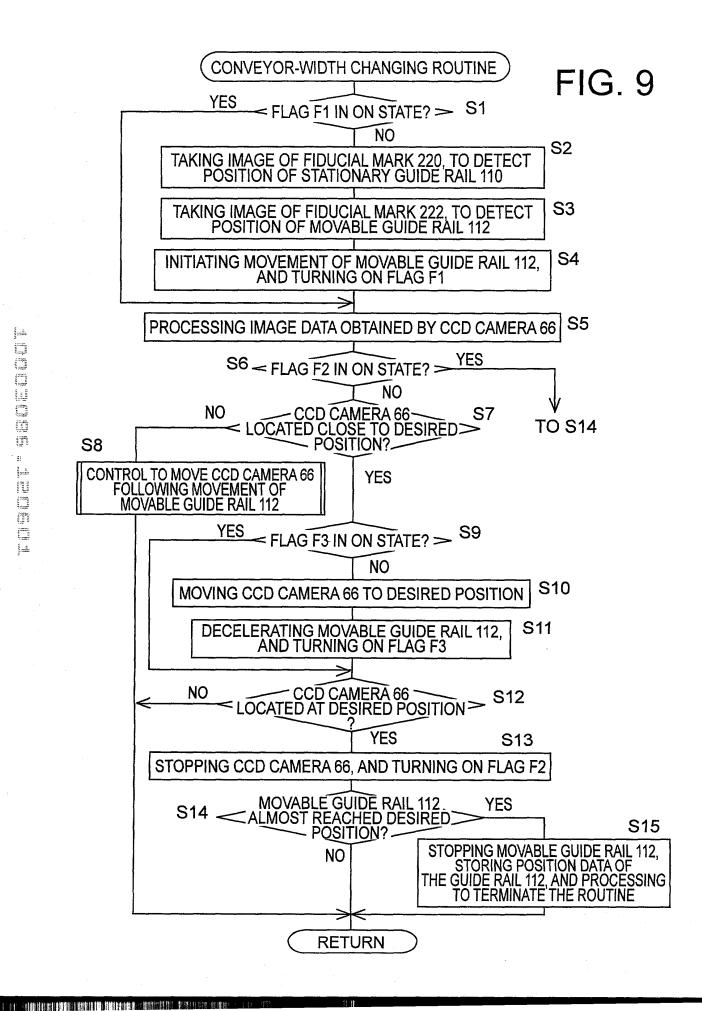
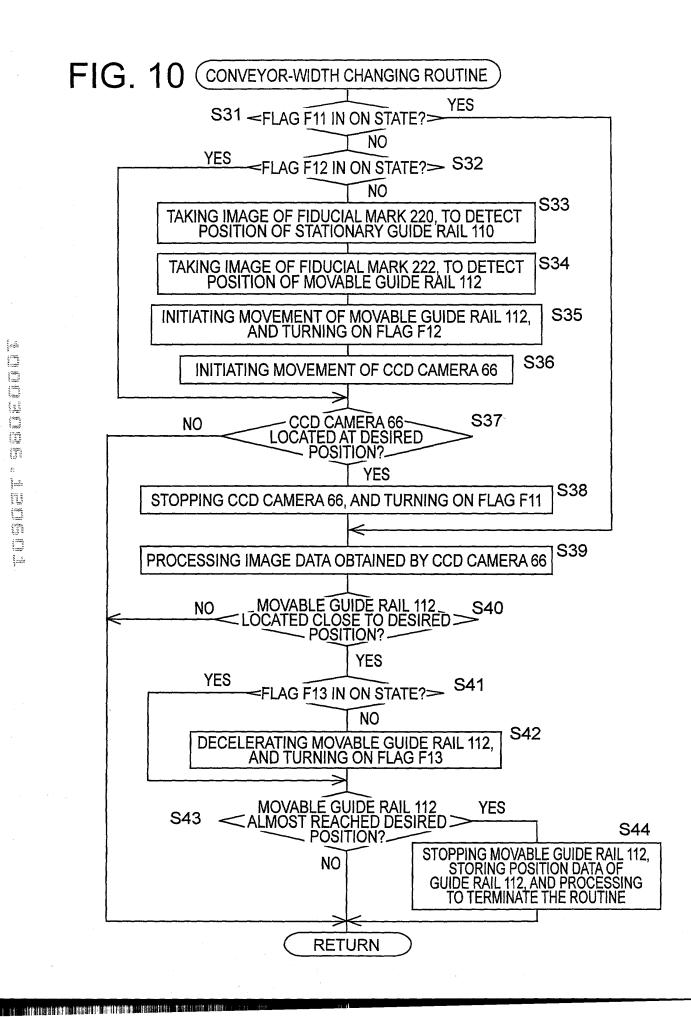


FIG. 8

RAM 256
PROGRAM MEMORY
MOVABLE-GUIDE-RAIL POSITION MEMORY
CONVEYOR WIDTH MEMORY
•
•
•
•
•
•





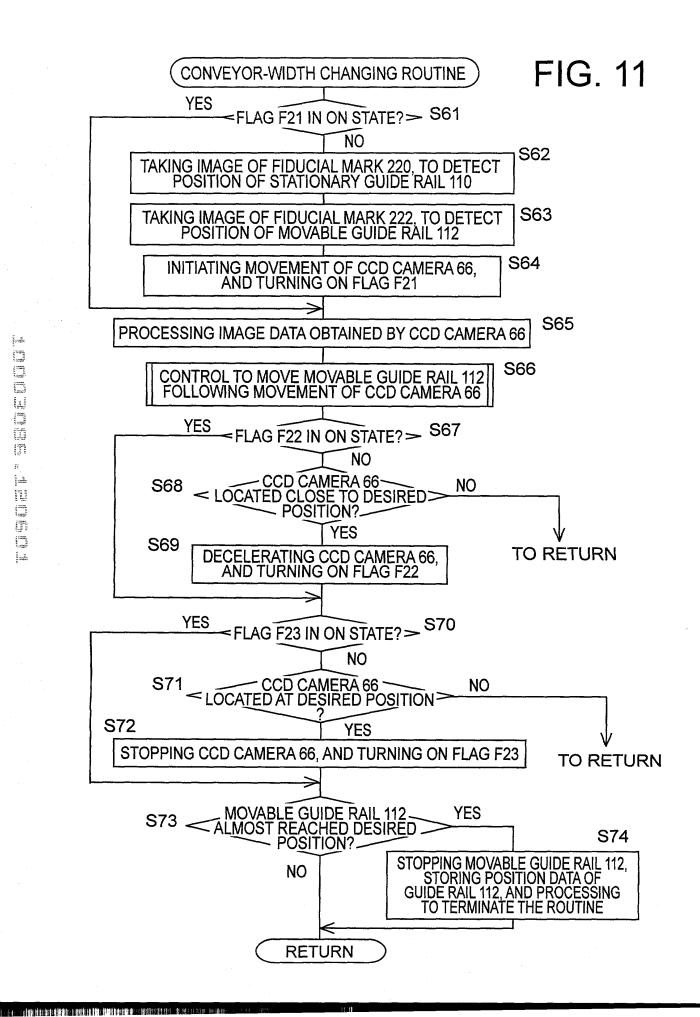
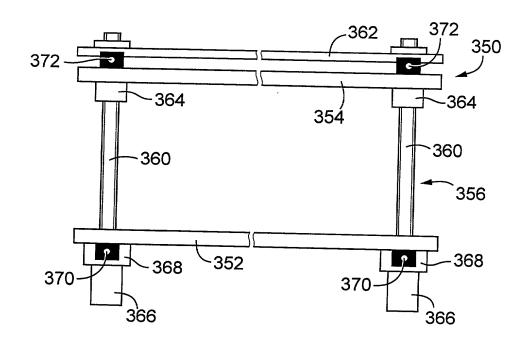


FIG. 12



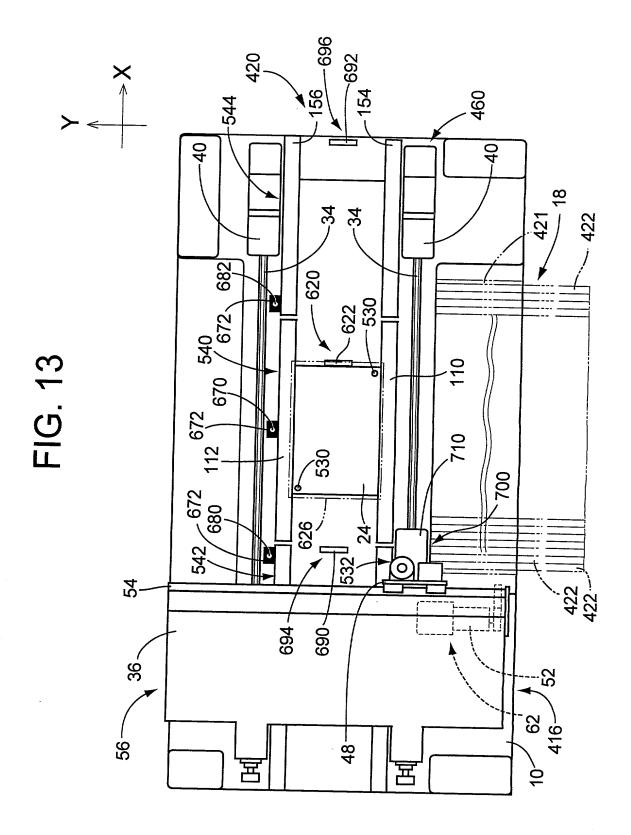


FIG. 14

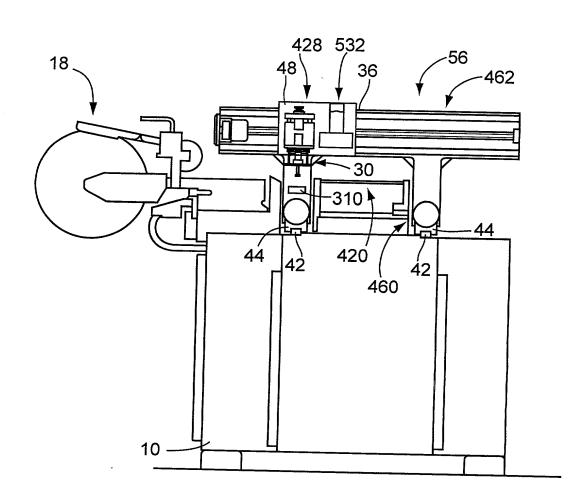


FIG. 15

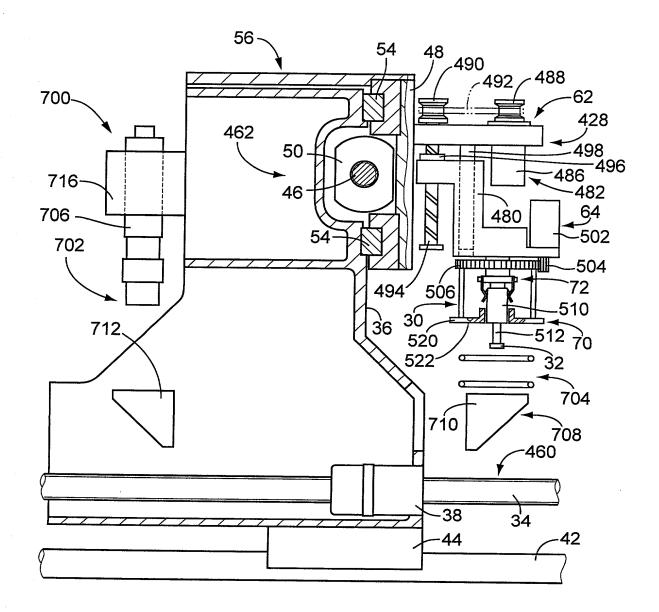
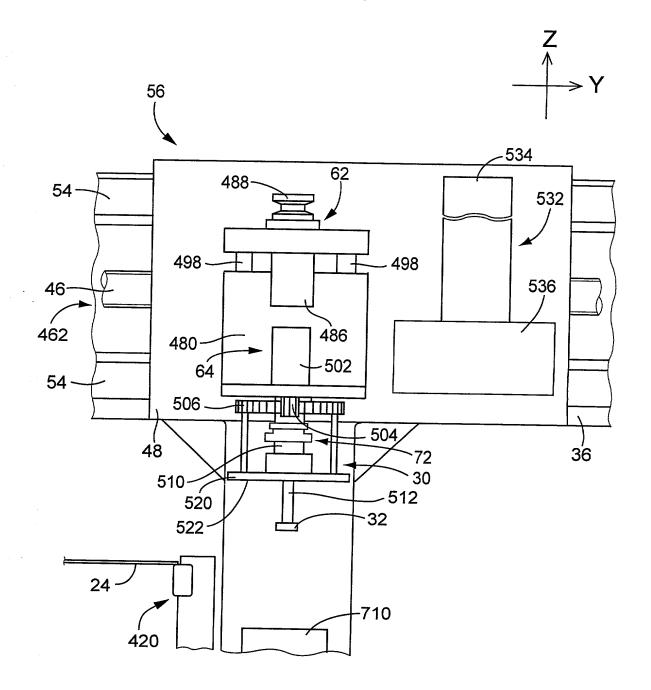
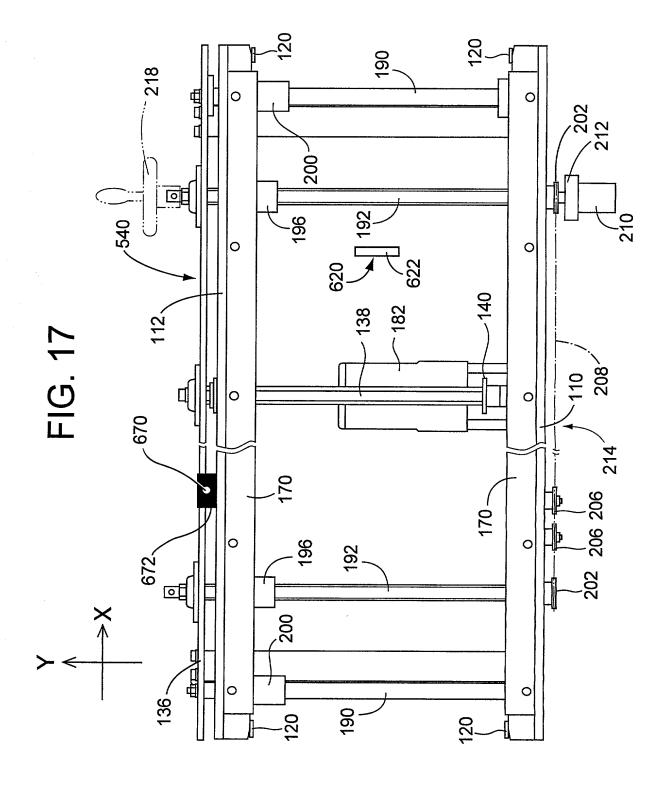
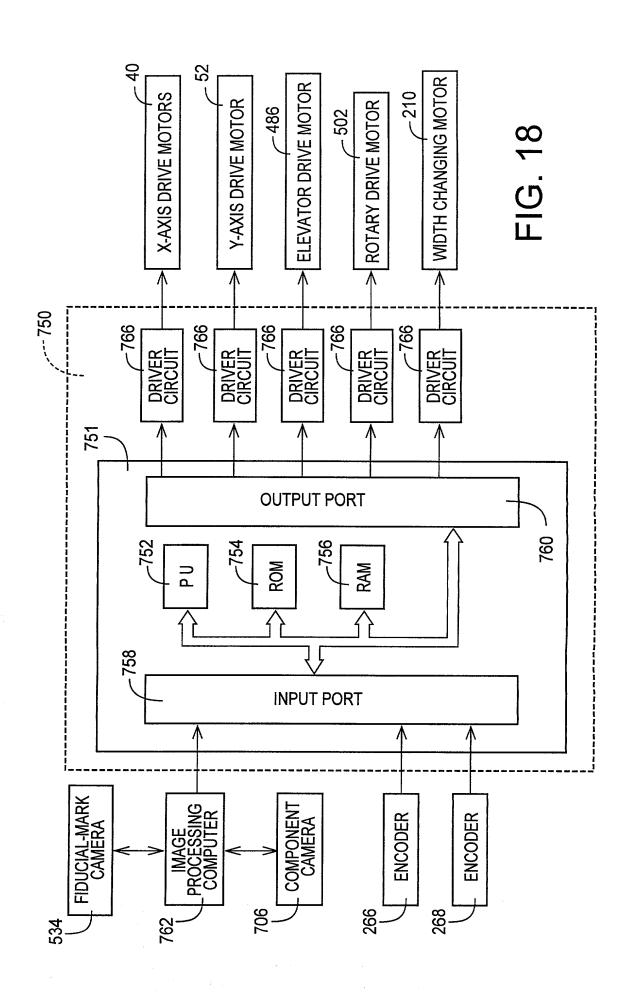


FIG. 16







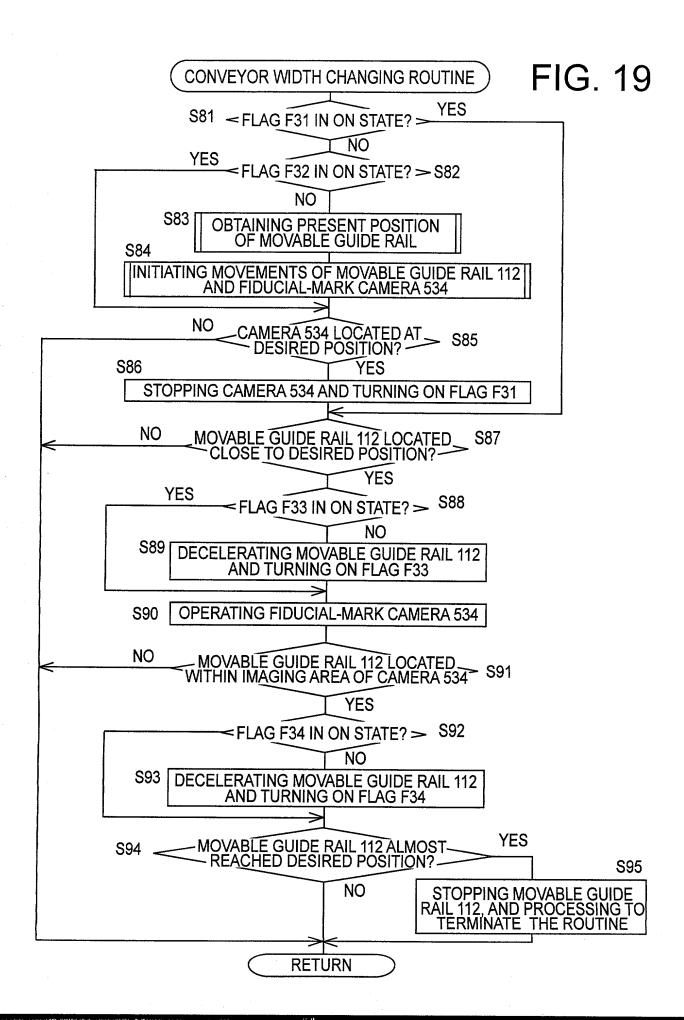


FIG. 20

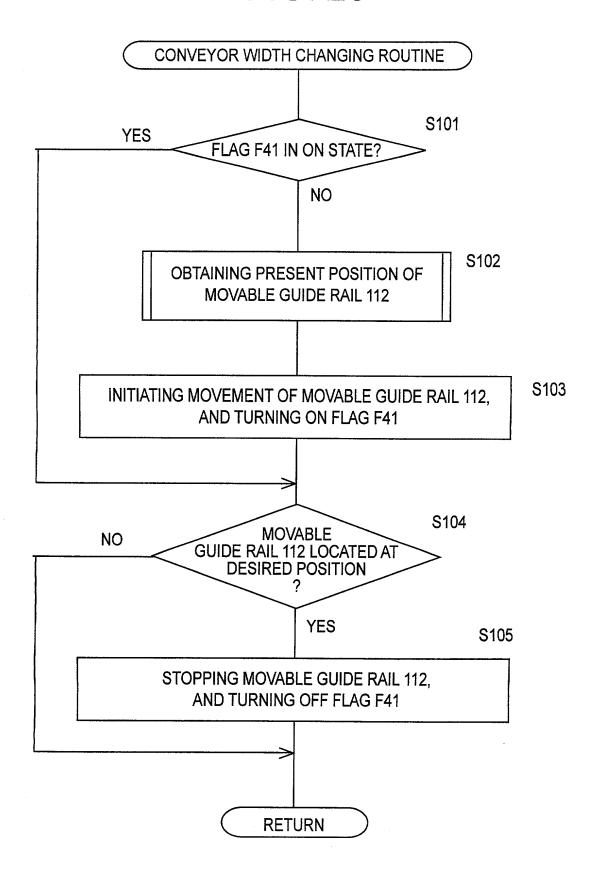


FIG. 21

